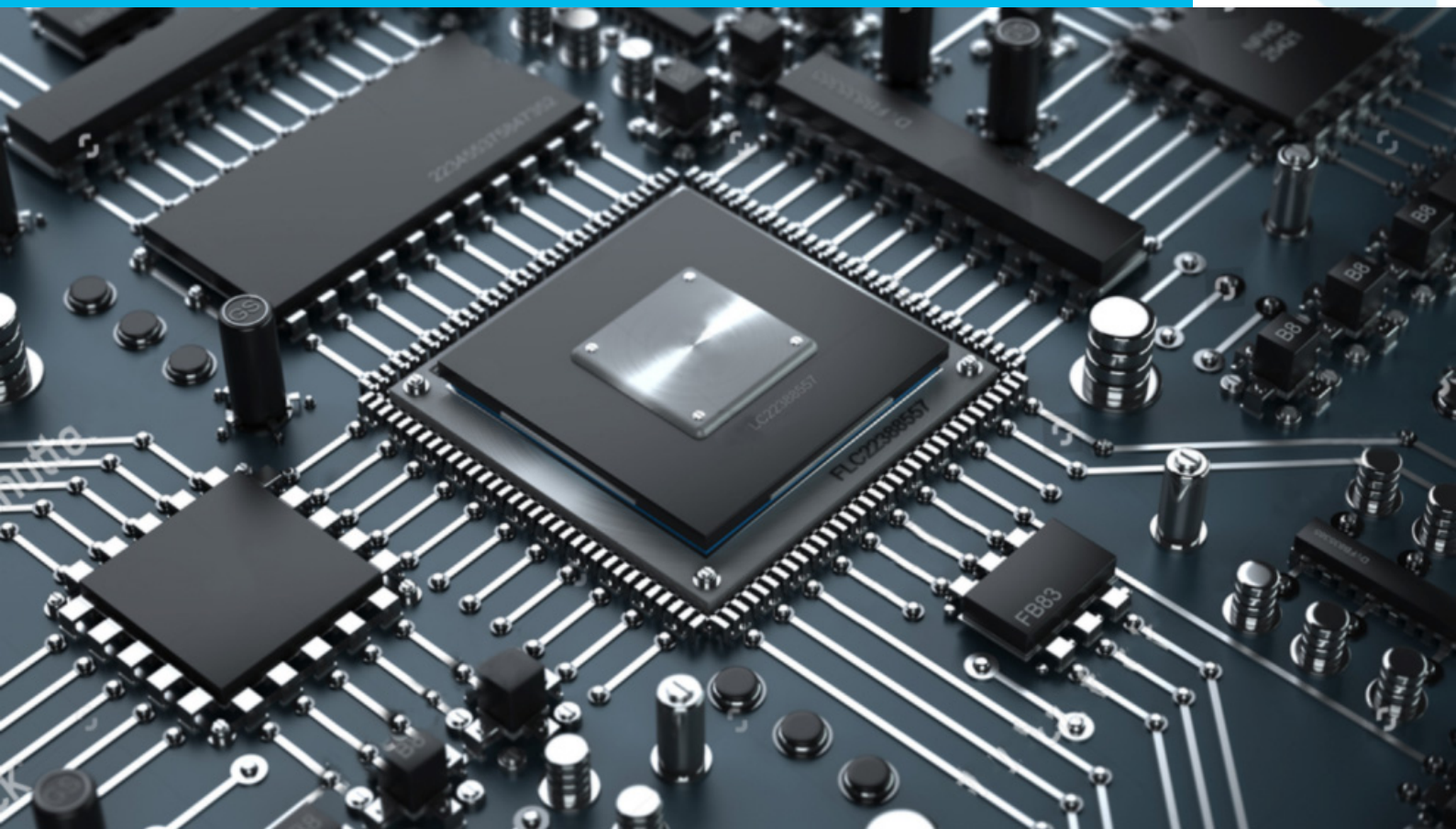


DOCD - DOWNLOAD AREA



PERFORMANCE

PERFECT SERVICE FOR FREE

The reason for the development of the **DoCD™**, was to provide our customers with the ability of easy system verification and software debugging, at no additional charges. Therefore, we have decided to add **the complete debug system** to each 8051/80251/80390 IP Core - **for free**

Now DCD's customers have the exceptional possibility, to obtain **the complete solution** for making their own 8051/80251/80390 based, SoC, with the ability to pre-silicon validation and post-silicon software debugging - **in one place**. It's really unusual opportunity for the designer, to have the ability to get a **high quality IP Core** and **unique on-chip debug tool**, from the same supplier.



LICENSING

Comprehensible and clearly defined licensing methods without royalty-per-chip fees make use of our IP Cores easy and simple.

- **Single-Site license option** - dedicated to small and middle sized companies which run their business at one place.

- **Multi-Site license option** - dedicated to corporate customers which operate at several locations. The licensed product can be used at selected company branches.

In all cases the number of IP Core instantiations within a project and the number of manufactured chips are unlimited. There are no restrictions regarding the time of use.

There are two formats of the delivered IP Core that you can choose from:

- VHDL or Verilog RTL synthesizable source code (called HDL Source code)

- FPGA EDIF/NGO/NGD/QXP/VQM (called Netlist)

HDL Source code is suitable for ASIC and FPGA projects. The Netlist license is intended for FPGA projects only.

CONTACT

Digital Core Design Headquarters:

Wroclawska 94, 41-902 Bytom, POLAND

E-mail: info@dcd.pl

tel.: +48 32 282 82 66

fax: +48 32 282 74 37

Distributors:

Please check: dcd.pl/contact-us/